

# Abstracts

## Solid-state high power RF oscillator

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*A. Gitsevich, D. Kirkpatrick and L. Dymond, Jr.. "Solid-state high power RF oscillator." 2001 MTT-S International Microwave Symposium Digest 01.3 (2001 Vol. III [MWSYM]): 1423-1426 vol.3.*

A new kind of RF solid-state oscillator design is observed. The oscillator has two feedback loops, which provide a safe working condition for the power transistor at high levels of RF output power. The oscillator uses a high power LDMOS transistor, which provides output power greater than 65 W and efficiency better than 65% at the operating frequency of 915 MHz. The oscillator is designed using microstrip lines and lumped components on Rogers R03010/sup TM/ substrate.

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